

# Model Reduction For Circuit Simulation (Lecture Notes In Electrical Engineering)

Model Order Reduction Techniques for Circuit Simulation by Luis Miguel Silveira Submitted to the Department of Electrical Engineering and Computer Science

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Lecture notes, homework assignments Explain the purpose of a simulator such as SPICE. a k : 15. Copyright 2003 UCLA Electrical Engineering Department.

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The International Journal for Computation and Mathematics in Electrical and Engineering, Lecture Notes in Circuit simulation; Graph algorithms; Model

<http://www.emeraldinsight.com/doi/ref/10.1108/03321641211267155>

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Abstract. Theoretical and practical aspects of model order reduction techniques for use in the context of circuit simulation are investigated, with particular

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