

# Model Reduction For Circuit Simulation (Lecture Notes In Electrical Engineering)

Model Reduction for Circuit Simulation researchers and users of model order reduction with Springer in the Lecture Notes in Electrical Engineering

<http://www.math.uni-hamburg.de/spag/zms/syrene/>

Some concept of Engineering Electrical Circuits are Main points of this lecture are: Circuit Simulation, Circuit Simulation - Engineering Electrical Circuits

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{The SPRIM algorithm for structure-preserving order reduction of general {Model Reduction for Circuit Simulation, Lecture Notes in Electrical Engineering}

<http://citeseerx.ist.psu.edu/viewdoc/summary?doi=10.1.1.295.7973>

Courses Electrical Engineering and Computer Science Lecture notes. Model Order Reduction I: 25: Model Order Reduction II:

<http://ocw.mit.edu/courses/electrical-engineering-and-computer-science/6-336j-introduction-to-numerical-simulation-sma-5211-fall-2003/lecture-notes/>

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of periodic descriptor systems using balanced {Model Reduction in Circuit Simulation, volume 74 of Lecture Notes in Electrical Engineering}

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Lecture notes, homework assignments Explain the purpose of a simulator such as SPICE. a k : 15. Copyright 2003 UCLA Electrical Engineering Department.

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Model Order Reduction Techniques for Circuit Simulation by Luis Miguel Silveira Submitted to the Department of Electrical Engineering and Computer Science

<http://dspace.mit.edu/bitstream/handle/1721.1/36451/31262695-MIT.pdf?sequence=2>

Comparison of Model Reduction Methods with Applications to Circuit Simulation Roxana Ionutiu, Sanda Lefteriu, and Athanasios C. Antoulas Department of Electrical

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The International Journal for Computation and Mathematics in Electrical and Engineering, Lecture Notes in Circuit simulation; Graph algorithms; Model

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